

DIGITAL RECORDER WITH TRICK PLAY OPERATION

This invention relates to the field of digital video recording and in particular to reproduction at speeds other normal.

BACKGROUND OF THE INVENTION

5 A digital video signal may be processed to form a bit stream having a reduced bit rate. Such processing for bit rate reduction may be implemented according to an MPEG compression method, and may be formatted for example, with a structure, such as that employed in a digital satellite system, for example DSS™ or the
10 proposed Grand Alliance or GA terrestrial system.

A consumer video recorder may be adapted to provide recording capabilities for both analog signals and digitally encoded signals processed for example, for compatibility with an MPEG standard and having for example GA or DSS™ signal formats. The
15 adapted consumer video recorder may be considered as two electronic systems within one box, sharing a recording mechanism, servo and control systems. A conventional analog recording method may be employed where an analog luminance signal component frequency modulates an RF carrier and an analog chrominance signal component
20 amplitude modulates a second RF carrier. When reproduced, the two modulated signals are demodulated, combined and coupled for video display.

Digital recording may be achieved by employing the consumer video recorder as a digital bit stream recorder/player. The
25 MPEG compatible bit stream, for example transport packets, may be recorded by reformatting and processing to facilitate recording and reproduction. In a playback mode the recording processes are effectively reversed, and the bit stream is restored to have the original MPEG format and time relationships.

30 The material cost of a consumer digital recorder may be minimized by the omission of MPEG processing or decoding. Thus, the recorder records and replays the MPEG packet stream without changes or additions. Hence the consumer digital recorder functions as a bit stream recorder for program delay or time shifting usage.

35 The MPEG compression method employs intra coded frames, or I frames, forward predicted frames, or P frames and a bidirectionally predicted frames, or B frames. These three types of

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SUMMARY OF THE INVENTION

A record and replay apparatus comprises a transducing means for reproducing a digital signal from a medium. A control means for controls a reproduction mode of the apparatus. A decoding means is coupled to the transducing means for decoding the digital signal and deriving therefrom a signal indicative of a specific picture type. The reproduction mode has a first period of reproduction at a normal play speed and a second period of reproduction at a speed greater than the play speed. The first and second periods alternating with the first period initiated responsive to the signal indicative of a specific picture type.

In a further inventive embodiment a record and replay apparatus is coupled at an input to a source of an MPEG bit stream signal for recording. A first transducing means records and reproduces a digital signal representative of the MPEG bit stream. A control means is coupled to the MPEG bit stream signal and generates a signal indicating an intra coded frame occurrence in the MPEG bit stream. A second transducing means records and reproduces a servo reference signal. A generating means is responsive to the indicating signal for generating a identifying signal for recording with said servo reference signal.

In yet a further inventive embodiment a recording and replay apparatus has a trick play reproduction mode comprising the steps of:

- a) initiating a play mode for I frame reproduction from a recorded media;
- b) initiating a fast play mode to pass the recorded media;
- c) resuming the play mode for reproduction of a subsequent I frame from the media; and,
- d) controlling transitions between the play and said fast play modes responsive to a fast play mode predetermined sequence.

In another inventive embodiment a recording and replay apparatus has a trick play reproduction mode comprising the steps of:

- a) initiating a play mode;
- b) determining an average number of control track pulses occurring between I frames;
- c) selecting a trick play mode;

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- d) counting control track pulses to determine an average value;
- e) testing a count for equality with the average number; and,
- f) initiating said play mode at count equality.

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BRIEF DESCRIPTION OF THE DRAWINGS

FIGURE 1 depicts an exemplary block diagram including various inventive embodiments.

FIGURE 2A shows tracks recorded on a medium employing inventive embodiments.

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FIGURES 2B - 2D depicts control track pulses, including various inventive embodiments, recorded on a medium.

FIGURES 3A - 3C show various control track pulse waveforms including pulse width modulation by supplementary information.

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FIGURE 4A shows the recorded pattern of FIGURE 2A.

FIGURE 4B shows an inventive tape speed control sequence facilitating trick play reproduction.

FIGURE 4C depicts a trick play frame sequence resulting from the control sequence illustrated in FIGURE 4B.

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FIGURE 5 is an exemplary flow chart illustrating an inventive trick play method for anticipating a track containing an I frame.

DETAILED DESCRIPTION

25 A simplified consumer digital audio video system is illustrated in FIGURE 1. A receiver, IRD 100, is depicted coupled to a receiving antenna 50, however, an RF modulated signal may be provided from a cable distribution network (not shown). The modulated packetized signal is tuned, demodulated and a user determined program separated within the receiver packet selector
30 block indicated as RCV. PKT. SEL. 55. An output from the receiver packet selector 55 comprises a transport packet stream of audio video and control data which is MPEG decoded by DECODER 117. The MPEG decoder operates in conjunction with a memory, MEM. 116, which stores several frames including the last I frame. In the event of an
35 MPEG data stream loss or mangled MPEG syntax, decoder 117 and memory 116 are controlled by controller 115 to sustain output signal generation by repeating a preceding frame from memory 116. Thus

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data stream loss or distortion, for example, due to rain fade, replay data loss, drop out or distortion, causes the last frame to be duplicated, producing a frozen or still frame image, for the duration of the data outage. The decoder generates audio and video signals which are coupled to output block O/P 118 which produces analog audio and video output signals, 101 and 104, which are coupled for monitoring by display 300.

Replay packet source 200, which may for example, conform to the D-VHS or DVC recording formats, is illustrated in the simplified exemplary block diagram of FIGURE 1. As described previously, cost considerations may dictate that digital recorder 200 does not include MPEG encoding and decoding. Hence digital recorder 200 functions as a bit stream recorder/player, and provides time shifting or program delay capability. The omission of MPEG decoding and the degree of complexity required to facilitate non-standard speed replay capability, renders image reproduction difficult at speeds other than play speed. In summary, receiver IRD 100 couples an MPEG compatible packet stream via digital A/V bus 112 for recording by recorder 200. Similarly a replay packet stream is coupled via bus 112 to receiver IRD 100 for MPEG decoding, and audio/video generation.

Player recorder 200, illustrated in FIGURE 1, receives demodulated transport packets from receiver IRD 100 via bi-directional data bus 112. The transport packet stream is formatted for bus transmission, for example as a superpacket with superpacket header, and coupled to data bus 112 via an interface port 110. The formatted transport packet is received at recorder 200 by means of interface port 210 which derives a record bit stream 211. The interface ports are controlled by a control data stream carried on a separate conductor included with data bus 112. Control signals may be derived from user input by activation of control switches (not shown), or by user generated remote control commands.

The record bit stream 211 is coupled for processing by recording block DIG. REC. 215. Digital recording block 215 includes a buffer memory which is utilized to smooth the intermittent delivery of transport packets to produce a signal more suited for recording. The buffered data stream is read from the buffer memory to form

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sync blocks which may be encoded with error detection / correction data words. The sync block formatted data stream may be modulated for recording as described, and coupled from recording block DIG. REC. 215 via a selector switch A4. Selector switches A4 and A5 are controlled responsive to the selected D-VCR operating mode. For example, switches A4 and A5 are shown positioned for digital operation, with digital recording signals routed by switch contacts DR, digital playback data routed by switch contacts DP. A record play switch A6 couples data stream 251a to and from transducer heads 251 located on rotating head assembly 250. Analog operation is facilitated by the alternate positions of switches A4 and A5, designated analog record AR, and analog playback AP. Sync block formatted data from recording block DIG. REC. 215 is coupled to rotating head assembly 250 for recording by heads 251 on a magnetic tape 249.

During replay operation, sync block formatted data is reproduced from the magnetic tape 249 by transducing heads 251 positioned on assembly 250. The recovered signal is demodulated by digital replay block DIG. REP. 220, to remove any recording channel modulation. The replay signal is then subject to error detection and correction by means of the data words inserted prior to recording. Following error correction, the sync block formatting is removed and the transport packet stream restored to have substantially the same the packet format and timing as that coupled to the recorder for recording. The transport packet stream from block DIG. REP. 220 is coupled to interface port 210 which, responsive to the recorder play mode couples the packet stream to the bi-directional data bus 112 for MPEG decoding by decoder 117.

Player recorder 200 includes a servo control system represented within dash line box 201. During analog operation synchronizing signals AS are separated from the analog signal to be recorded in analog record processor block 240, and are coupled to synchronize sync generator 285. Synchronization is required to ensure that servo block 280 controls rotation of the capstan motor 255 and rotary head motor 246 in synchronism with the analog video signal. In simple terms the capstan must pull the tape at a constant rate locked to the analog signal and the rotary head must write the

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analog signal at predetermined media locations. However, unlike analog signals, the MPEG packet stream cannot readily provide synchronizing signal AS required for servo system control of recorder 200. Hence during digital operation servo system 201 utilizes
5 synchronizing signals generated by synchronizing generator 285 which is referenced to a crystal oscillator X. Thus sync generator 285 provides stable control signals which ensure that head assembly 250 is locked and synchronized at 30 revolutions per second and the tape is transported at substantially the same speed as during analog
10 operation.

In addition to smoothing the intermittent delivery of transport packets, the buffer memory of recording block 215, enables the output signal 216 to be recorded at predetermined locations on tape 249. For example, buffer memory may be read out
15 in synchronism with rotation of head assembly 250. Similarly each video data sync block, formatted from the buffer may be read out to have a specific location in each recorded track. Synchronization of the buffer memory of block 215 is provided by a control signal, CTRL. 1 generated by controller 205.

As is known in analog recording, the recording head assembly 246, 250 and capstan motor assembly 253, 555 are servo controlled to rotate in synchronism one with the other, and in synchronism with an analog sync signal. During digital recording, the head and capstan motors may be referenced to the crystal controlled
20 synchronizing generator 285. To ensure replay synchronism between the capstan and video head drum a control track is recorded along a bottom tape edge as shown in exemplary FIGURE 2A. Control track pulses are, for example, derived from the head drum and consequently have a repetition rate 30 Hz. Thus for each head drum
25 revolution a control track pulse marks one track, for example having an azimuth angle $\emptyset 1$. Since the head drum employs two heads spaced by 180 degrees, nominally 60 tracks are recorded per second. The recorded control track signal is illustrated in FIGURE 2B and shows a control track pulse marking the start of alternate tracks. Twelve
30 frame groups of pictures are illustrated in FIGURE 2A, and for simplicity, longitudinal audio tracks and transverse track angles are not shown.
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Reproduction at speeds other than normal play speed may be advantageously facilitated by use of a stored automated play control sequence which alternates recorder control modes between normal play speed and fast play speed operation. During fast play operation the capstan pinch roller 254 remains engaged with capstan 253 and the rotational speed is increased and maintained under servo control. For example in FIGURE 2A, 12 frame groups of pictures are illustrated, repeating on average every 24 tracks with the first two tracks depicted with I frame data. If normal play speed operation is initiated from any location in the track patterns of FIGURE 2A, I frame data must be recovered within 24 tracks or twelve control track (CT) pulses. Thus in the automated control sequence described above, normal play speed operation may be activated for a duration of 12 CT pulses and fast play, for example at four times speed, selected for approximately 256 control track pulses. Hence this exemplary automated trick play sequence will sample the recorded tracks approximately every 2 seconds, which corresponds to approximately 8.5 seconds of elapsed tape or program time at 30 Hz drum rates or about 10 seconds with 25 Hz speeds. Thus a forward search trick play mode is provided which comprises a series of still I frames images spaced by nominally 8 - 10 seconds of elapsed program time. During fast play periods the reproduced MPEG syntax may be mangled and the data stream lost, however, IRD decoder 117 and memory 116 advantageously sustain the output signal by repeating the last preceding frame from memory. A reverse search trick play mode may be similarly provided where capstan rotation is reversed for the period of the exemplary 256 control track pulses. However, such reverse tape motion requires controlled operation of the take up and supply tape reels to prevent slack, or over tensioning causing tape damage.

In a further advantageous embodiment controller 205 receives control data signal 202 from bus interface 210 via control line 202a. Control data signal 202 is generated within IRD 100 and includes a signal component IF which marks the occurrence of an I frame in the transport packet stream coupled for recording. Intra-frame marker signal IF may be communicated to recorder 200 by means of a header added to a superpacket employed for transport

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packet bus communication, or as a separate control signal coupled via bus 112. The superpacket header is read by digital recording block 215 and IF marker extracted and sent to controller 205 via control line 205a. In an alternative arrangement digital recording block 215 may filter bit stream 211 to determine an intra-frame occurrence by extracting picture header information from GOP and sequence headers. Controller 205 may employ the I frame marker signal IF together with head and capstan phase information to form control signal CTRL 1. Control signal CTRL 1 controls read out from the buffer memory of block 215 to ensure that I frame data recording is initiated in tracks of the same recorded azimuth. Thus the inventive use of I frame marker IF ensures that each I frame recording is initiated in tracks of the same recorded azimuth angle.

In exemplary FIGURE 2A, a magnetic tape 249 is shown recorded with an MPEG representative signal having a group of pictures size of 12 frames. Intra-coded data is shown with horizontal shading in FIGURE 2A, and is depicted with recording initiated in track 1, with an azimuth phase angle $\emptyset 1$ and continuing into track 2 with an azimuth angle $\emptyset 2$. Forward predicted, or P frame data, is recorded in track 3 with an azimuth angle $\emptyset 1$, and is shown shaded diagonally. The remaining 9 frames of this exemplary 12 frame GOP are written in the following unshaded track area. Since the recording head drum rotates at 1800 RPM and has two heads diametrically opposed, nominally 60 tracks are written per second. Thus the GOP size of 12 frames is illustrated occupying 24 tracks. However, since the amount of data present in each compressed frame varies in accordance with motion and scene complexity, the actual duration of individual frames may vary. Hence the duration of a GOP may vary and thus require that unfilled recording track capacity be filled with zeros. In the simple recorded track patterns of exemplary FIGURE 2A, I frame data may or may not completely fill the data capacity provided by tracks 1 and 2, hence P frame data recording may be initiated in track 2 and continue in track 3. Similarly for the remaining frames of the group of pictures, which are written consecutively and may be distributed between adjacently recorded tracks. In addition, GOP size is user defined and may be changed at will with a new GOP being initiated with an I frame. Hence the

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advantageous coupling of I frame marker IF ensures that each I frame recording is always initiated with the same azimuth phase. For example, in FIGURE 2A I frame tracks have an azimuth $\emptyset 1$ and are indicated with a control track pulse, as shown in FIGURE 2B. Control track pulses are, for example, derived from the head drum, have a repetition rate 30 hertz and mark one track of each pair, for example $\emptyset 1$, for each head drum revolution.

In a further inventive embodiment, trick play reproduction may be advantageously facilitated by controlled reproduction of only tracks containing intra-coded or I frame data. Since I frame marker IF, determined the recorded track selection i.e. azimuth $\emptyset 1$ or $\emptyset 2$ and control track pulses mark alternate tracks, tracks containing I frame data may be advantageously identified for reproduction. To facilitate replay identification of I frame data, specific control track pulses are advantageously modulated during recording with signal A which is derived, as described previously, from the occurrence of I frame data in recording bit stream 211. Thus signal A modulates the control track signal to mark tracks containing I frames. FIGURE 2C shows an enlarged section of the control track signal depicted in FIGURE 2B. The section shown in exemplary FIGURE 2C is centered, for example, about an I frame recorded in track 25, and illustrates a sequence of reproduced control track pulses. Pulse A, shown with a solid black section, marks I frame track 25 and has been advantageously pulse width modulated during recording to specifically indicate I frame data tracks.

In the VHS format control track pulse width modulation is known and used to facilitate searching with systems such as Video Index Search Signal (VISS) or Address Search Signal or (VASS). However to summarize, control track pulse width modulation operates as follows. During replay the rising edges of the control track signal, depicted by arrow heads in FIGURE 3A, are used for playback speed control. However the trailing, non-time critical edge of the control track pulse may be controllably phase shifted or repositioned. Data may be encoded by varying the phase or position of the trailing edge of the recorded control track signal between two distinct phases, or positions representing digital true and false or one and zero value data bits.

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FIGURE 3A depicts a standard control track pulse signal having a nominal pulse width duty cycle of between 50% to 60%. FIGURE 3B depicts a phase modulated control track pulse showing both logical "1" and logical "0" values. A logical "1" is encoded, for example, by advancing the phase of the trailing edge, thus shortening the positive portion of the control pulse to a duration of about 27.5% of the pulse repetition period. A logical "0" value delays the trailing edge as compared to a nominal 50% duty cycle, for example to produce a pulse of 60% duration. FIGURE 3C illustrates phase modulated control track pulses shaped for recording. Simple timing and gating circuits can distinguish between pulses having a duty cycle that is longer or shorter than a reference duration (e.g., 50%).

Additional control track identification may be advantageously provided to indicate tracks containing, for example, forward predicted frames, P frames or bi-directional encoded frames, B frames. Such additional data may indicate P and or B frames by additional pulse coding between 28% to 60 % duty cycle of the control track pulse. Such P and or B frame indicators may, to simplify detection, follow and be enabled by, an occurrence of a preceding I frame coded control track pulse. For example FIGURE 2D shows an enlarged section of the control track signal is centered about I frame track 25 as depicted in FIGURE 2A. A sequence of reproduced control track pulses are illustrated in FIGURE 2D with pulse A marking 25, and pulse B, shown with horizontal shading, advantageously width modulated to indicate P frame data tracks, for example track 27.

The advantageous use of control track pulse modulation enables recorded tracks to be identified during reproduction at play speed and speeds other than play speed. Thus specific tracks, for example, intra coded data or intra coded and forward predicted data, may be identified and reproduced. Tracks identified as non-decodable in trick play, and hence unwanted, may be passed with increased tape speed. By identifying recorded track content, an inventive method of trick play reproduction is facilitated where decodable data, for example, intra coded, or intra coded and forward predicted data may be replayed at normal speed, with unwanted data tracks being passed at an increased tape speed. The media and recorded signals of FIGURE 2A are shown in FIGURE 4A. FIGURE 4B

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illustrates an exemplary automated trick play tape speed profile having areas of differing tape speed, S, P, A, F, and D occurring at various times. For example, area S corresponds to a stopped tape condition prior to reproduction. Area P represents normal play speed operation which is sustained for the duration of the intra coded data, which in this example is assumed to be contained in two recorded tracks, 1 and 2. Since the modulated A pulse marks the start of an I frame, the actual recorded duration is not marked, hence the play mode, indicated by area P, may be sustained for two or more control track counts. Following the control track count determining area P the tape is accelerated, during area A to be transported at a higher speed, for example four times. The exemplary automated trick play tape speed profile of FIGURE 4B illustrates a fast play condition, area F, of duration nominally 5 tracks or approximately 2 CT pulse counts. Such a short duration may be difficult to implement due to the mechanical constraints of capstan inertia and tape and reel masses. However, the automated speed profile illustrated in FIGURE 4B may be modified to accommodate specific recorder mechanisms. The automated trick play sequence illustrated in FIGURE 4B may result in a sequence of decoded frames as depicted, horizontal, vertical and temporally in FIGURE 4C. Clearly the number of repeated frames is determined by the availability of a newly reproduced decodable data stream. If only I frames of FIGURE 4A were reproduced the program would be viewed at twelve times normal speed, assuming a 12 frame GOP. If a reproduced frame is held or repeated for approximately the duration of a GOP then there is no actual or perceived speed up in program delivery and the recorded moving images are effectively represented as a series of stills. Thus in example of FIGURE 4A the program delivery rate is increased by slightly more than two times. The reproduced occurrence of I frame marker pulse A may be used to initiate a count of succeeding non-modulated control track pulses to determine or predict the occurrence of the next automated play sequence. Fast play duration in excess of one GOP may be facilitated by counting A pulses then decelerating to assume a normal play speed prior to the occurrence of the next I frame.

As previously described, the control track signal may be modulated to yield pulse A, which may advantageously be further

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results in a YES. The YES at step 400 sets, enables or loads a counter at step 500 with the value (N - D). The counter is decremented at step 600 with reproduced control track pulses and tested at step 700 for a zero value. The trick play mode is maintained for non-zero counts by a NO at step 700 which loops back to step 400. However when step 700 tests YES the next track containing a recorded I frame is anticipated and a play mode is initiated at step 800. Steps 400 to 800 are repeated by the occurrence of the reproduced I frame or control track marker pulse A which sets or loads the counter at step 500. The trick play mode is terminated when step 400 tests NO, for example, responsive to user command.

The method depicted in exemplary flow chart of FIGURE 5 employs control track pulses which marked during recording to indicate the recorded occurrence of an I frame. However in another inventive embodiment the exemplary method of FIGURE 5 may be utilized with recordings without control track I frame marker pulses. Tapes without control track marker pulse A may be played following the steps depicted. However, determination of average GOP size, step 200, in terms of control track pulse count is facilitated by means of I frame markers or pulses, for example signal IFR derived from the derived from the decoded reproduced MPEG data stream by, for example IRD 100. The replay I frame signal IFR is coupled, as has been described, to controller 205 and is processed to establish an average value of GOP size. When trick play operation is selected an automated trick play control sequence is read from memory with the control sequence initiated by the occurrence of I frame signal IFR which sets the counter at step 500. The period between normal speed reproduction being determined by the processed average GOP value as described earlier. Thus recordings without I frame encoded control tracks may be replayed in a trick play operating mode.

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